

*Sub C1*

In the claims:

**Please amend claims 1, 4, 8, 9 and 13 as follows:**

1. (AMENDED) An apparatus for converting signals of a first preselected voltage level to a second preselected voltage level, comprising:

a transistor having an enable terminal, an input terminal, and an [an] output terminal [and], said input terminal receiving [being adapted to receive] said signals of the first preselected voltage level, and said output terminal delivering [deliver] said signals of the second preselected voltage level;  
a capacitor coupled across said input and output terminals of said transistor; and  
a resistive element having a first end portion [adapted to be] coupled to a voltage supply.

*Sub C2*

4. (AMENDED) An apparatus, as set forth in claim 3, including a pull-up transistor [adapted to be] coupled between the input of said inverter and a voltage supply, and having an enable terminal coupled to the output of said inverter.

*Sub C3*

8. (AMENDED) An apparatus for converting signals of a first preselected voltage level to a second preselected voltage level, comprising:

a pass gate transistor having a gate, source, and drain, said drain receiving [and being adapted to receive] said signals of the first preselected voltage level, [and deliver] said source delivering said signals of the second preselected voltage level, said gate [being adapted to be] coupled to a voltage supply having a third preselected voltage level;  
a capacitor coupled across said source and drain of said pass gate transistor; and  
a pump coupled to the gate of said pass gate transistor, said pump being [adapted] configured to temporarily increase the voltage level applied to said gate.

*a3*  
*Concl'd*

9. (AMENDED) An apparatus, as set forth in claim 8, wherein said pump includes a resistive element [adapted to be] coupled between the gate of said pass gate transistor and said voltage supply, and a capacitor coupled to the gate of said pass gate transistor and [being adapted to receive] receiving said input signal.

*Sub C4*  
*Ad*

13. (AMENDED) An apparatus for converting an input signal of a first preselected voltage level

to a second preselected voltage level, comprising:

a pass gate transistor having a gate, source, and drain, said drain receiving [and being adapted to receive] said signals of the first preselected voltage level, [and deliver] said source delivering said signals of the second preselected voltage level, said gate being coupled to a voltage supply having a third preselected voltage level; a capacitor coupled across said source and drain of said pass gate transistor; and means for temporarily increasing the voltage level applied to said gate.